**PATENT** 

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

## REISSUE APPLICATION

U.S. Patent No.:

5,528,482

Issued:

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Applicant:

Allen Frank Rozman

Title:

Low Loss Synchronous Rectifier for Application to Clamped-Mode Power

Converters

Commissioner of Patents and Trademarks Washington, D. C. 20231

Box: Reissue

Sir:

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## **INFORMATION DISCLOSURE STATEMENT**

Pursuant to the duty of disclosure under 37 C.F.R. § 1.56, Applicant submits this statement. This submittal is made in accordance with 37 C.F.R. §§ 1.97 and 1.98 and § 609 of the Manual of Patent Examining Procedure. The patents, publications and other information herein are listed below and on the attached Form PTO-1449. Copies of the listed references are submitted herewith.

U.S. Patent No.	<u>Date</u>	<u>Inventor</u>
5,541,828	07/30/96	Rozman
5,353,212	10/04/94	Loftus, Jr.
5,291,382	03/01/94	Cohen
5,282,123	01/25/94	Boylan, et al.

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5,231,563	07/27/93	Jitaru
5,179,512	01/12/93	Fisher, et al.
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5,126,651	06/30/92	Gauen
5,099,406	03/24/92	Harada, et al.
5,066,900	11/19/91	Bassett
4,975,821	12/04/90	Lethellier
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0 508 664 A1	EPO	10/14/92
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## Other References

"A Mosfet Resonant Synchronous Rectifier for High-Frequency DC/DC Converters" by Wojciech A. Tabisz, Fred C. Lee and Dan Y. Chen; 1990 IEEE; pp. 769-779

<sup>&</sup>quot;Constant Frequency, Forward Converter with Resonant Transition" by Ionel Dan Jitaru; HFPC; June 1991 Proceedings; pp. 282-293

"Practical Application of Mosfet Synchronous Rectifiers" by James Blanc; 1991 IEEE; pp. 495-501

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"Study of the Applicability of Self-Driven Synchronous Rectification to Resonant Topologies" by J.A. Cobos, J. Sebastian, J. Uceda, E. de la Cruz, and J.M. Gras; 1992 IEEE; pp. 933-940

"A Simple and Efficient Synchronous Rectifier for Forward DC-DC Converters" by N. Murakami, H. Namiki, K. Sakakibara and T. Yachi; 1993 IEEE; pp. 463-468

"5.6 Mosfets Move In On Low Voltage Rectification (TA84-2)" 1984; pp. 5-69 - 5-86

"Improving Power Supply Efficiency with Mosfet Synchronous Rectifiers" by Richard S. Kagan and Min-hwa Chi; Power Concepts, Inc. 1982; pp. 1-5

APEC '92 publication entitled "A New High Frequency, Zero-Voltage Switched, PWM Converter" by Ionel Dan Jitaru; 1992 IEEE; pp. 657-664

"Design Techniques for Transformer Active Reset Circuits at High Frequencies and Power Levels" by Bruce Carsten; HFPC - May 1990 Proceedings; pp. 235-246

"5.6.1 Using Power MOSFETs as High-Efficiency Synchronous and Bridge Rectifiers in Switch-Mode Power Supplies (TA83-1)"; 1984; pp. 5-87 - 5-94

"High Power SMPS Require Intrinsic Reliability" by Bruce Carsten; PCI 1981 Proceedings; pp. 118-133

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"Power Mosfet for Switching Regulator" by S. Ikeda, Y. Usanaga, H. Yoshida; 1982 IEEE; pp. 212-215

"High Power and High Switching Frequency DC-to-DC Converter Using Saturable Inductor Commutation and Switched Snubber" by Koosuke Harada and Hiroshi Sakamoto; 1991 IEEE; pp. 148-154

"Principles of Solid-State Power Conversion" by Ralph E. Tarter; 1985; pp. 544-547

"Buck Quasi-Resonant Converter Operating at Constant Frequency: Analysis, Design and Experimentation" by Ivo Barbi, J.C. Bolacell, D.C. Martins, F.B. Libano; 1989 IEEE; pp. 873-880

The Applicant sees the following patents as being more material to the patentability of the claims of the present application.

U.S. Patent No.	<u>Date</u>	<u>Inventor</u>
5,535,112	07/09/96	Vazquez Lopez, et al.
5,434,768	07/18/95	Jitaru, et al.

Applicant hereby expressly reserves the right to swear behind the effective dates of any of the above Patents and to question the relevance and materiality of the Patents and Publications listed herein, in whole, in part, or in combination, subsequent to filing this Information Disclosure Statement.

Respectfully submitted,

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